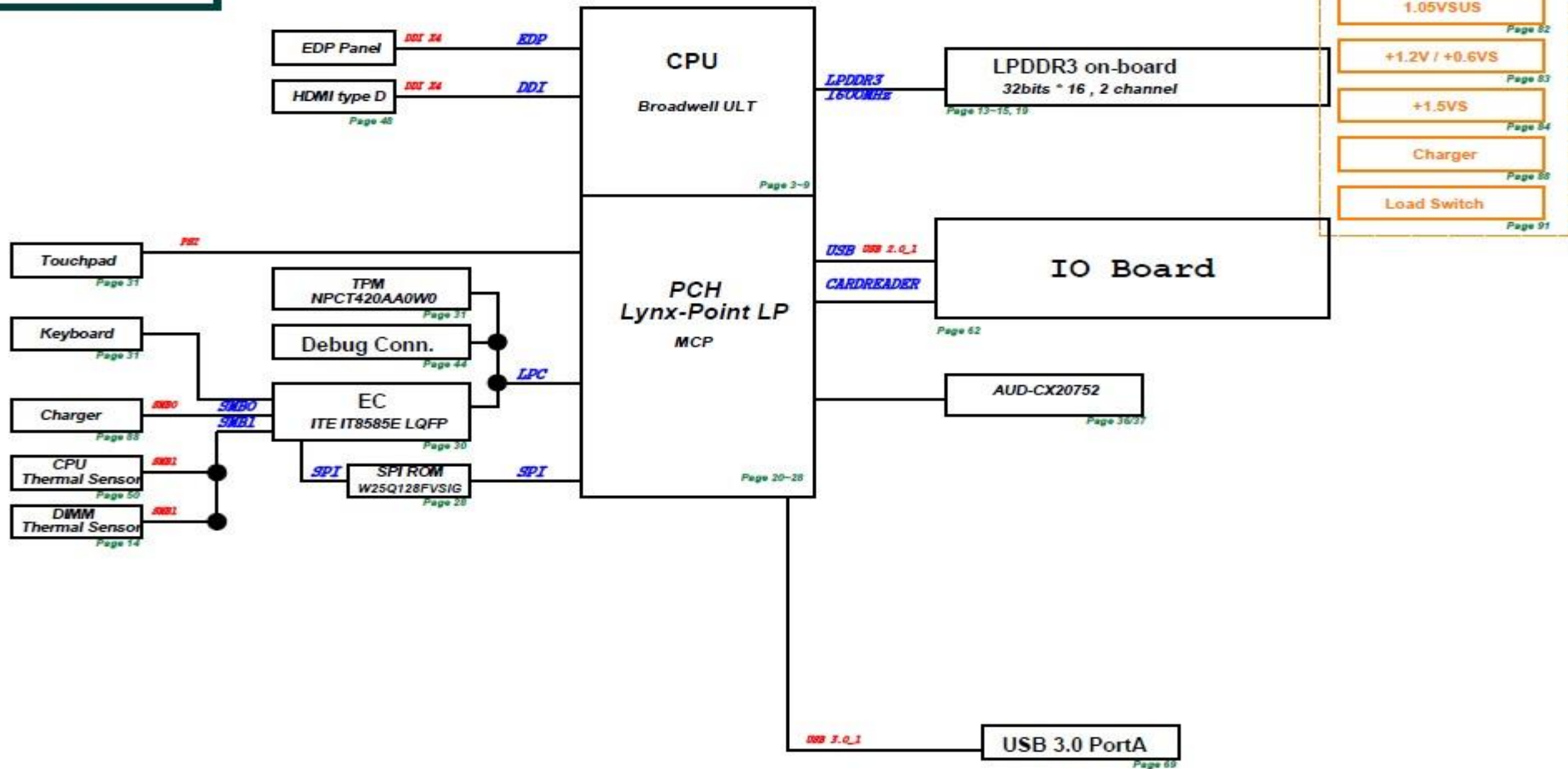


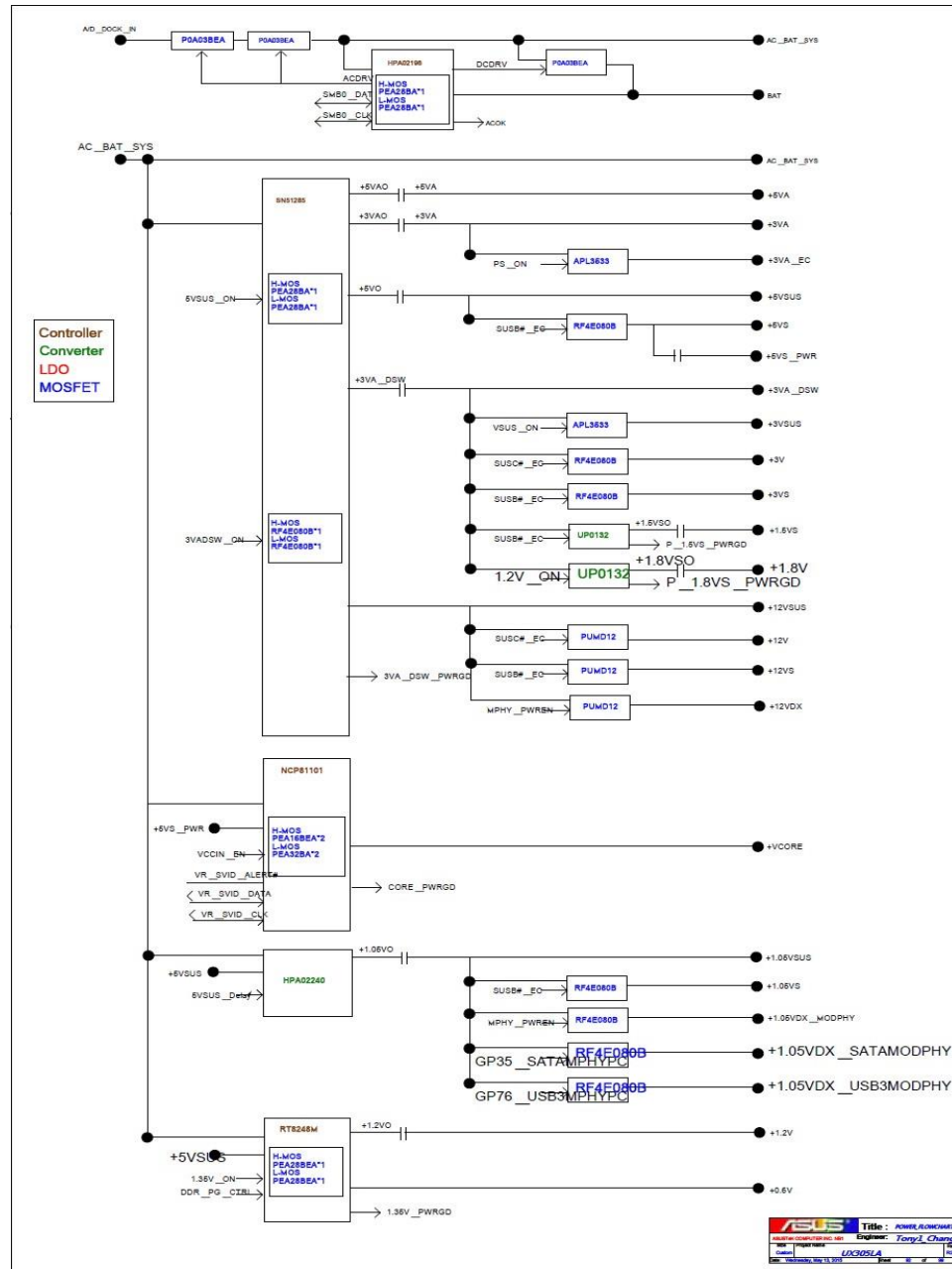
BLOCK DIAGRAM

UX305LA SCHEMATIC Revision R2.0

BLOCK DIAGRAM

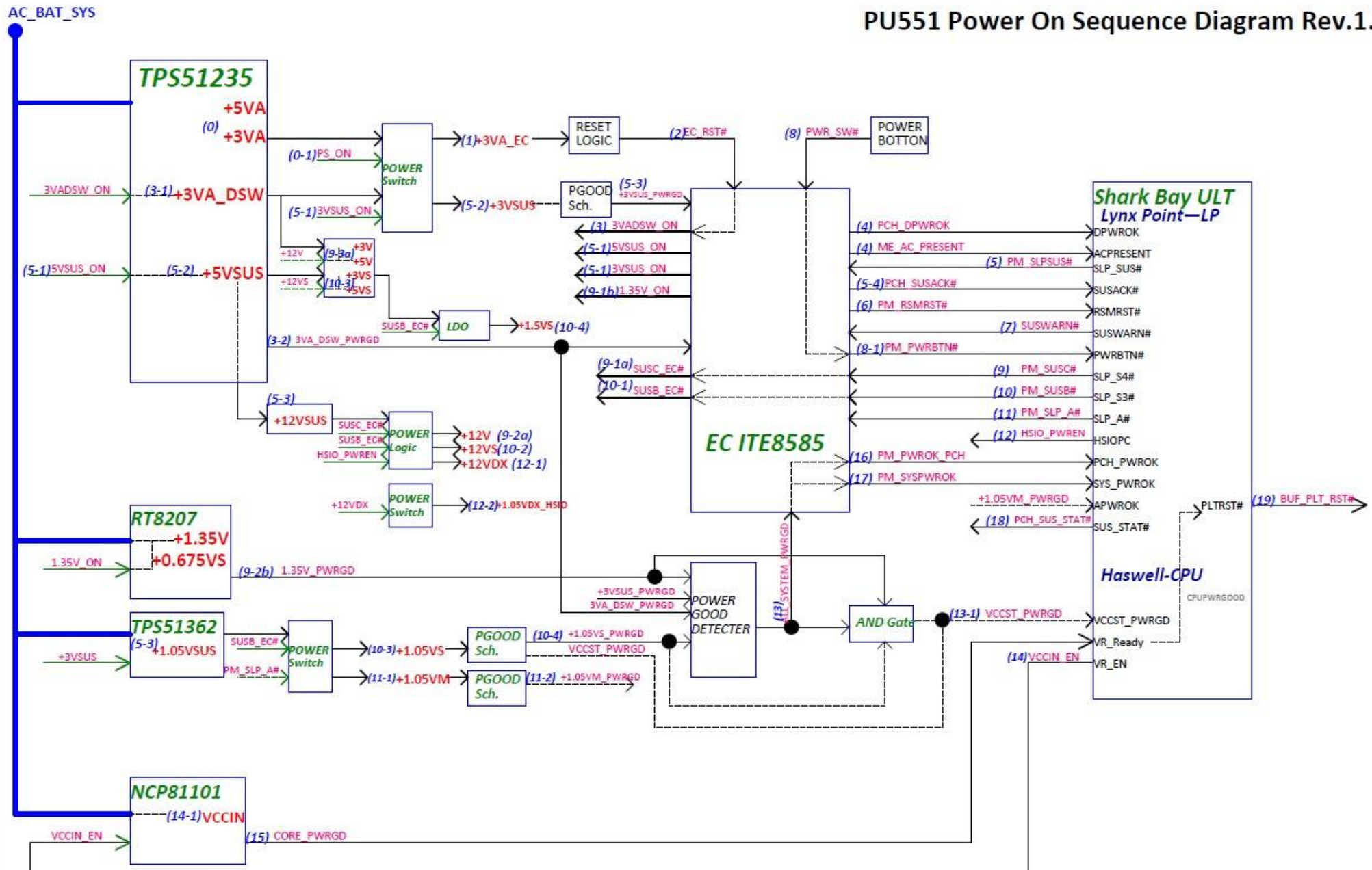


POWER FLOW



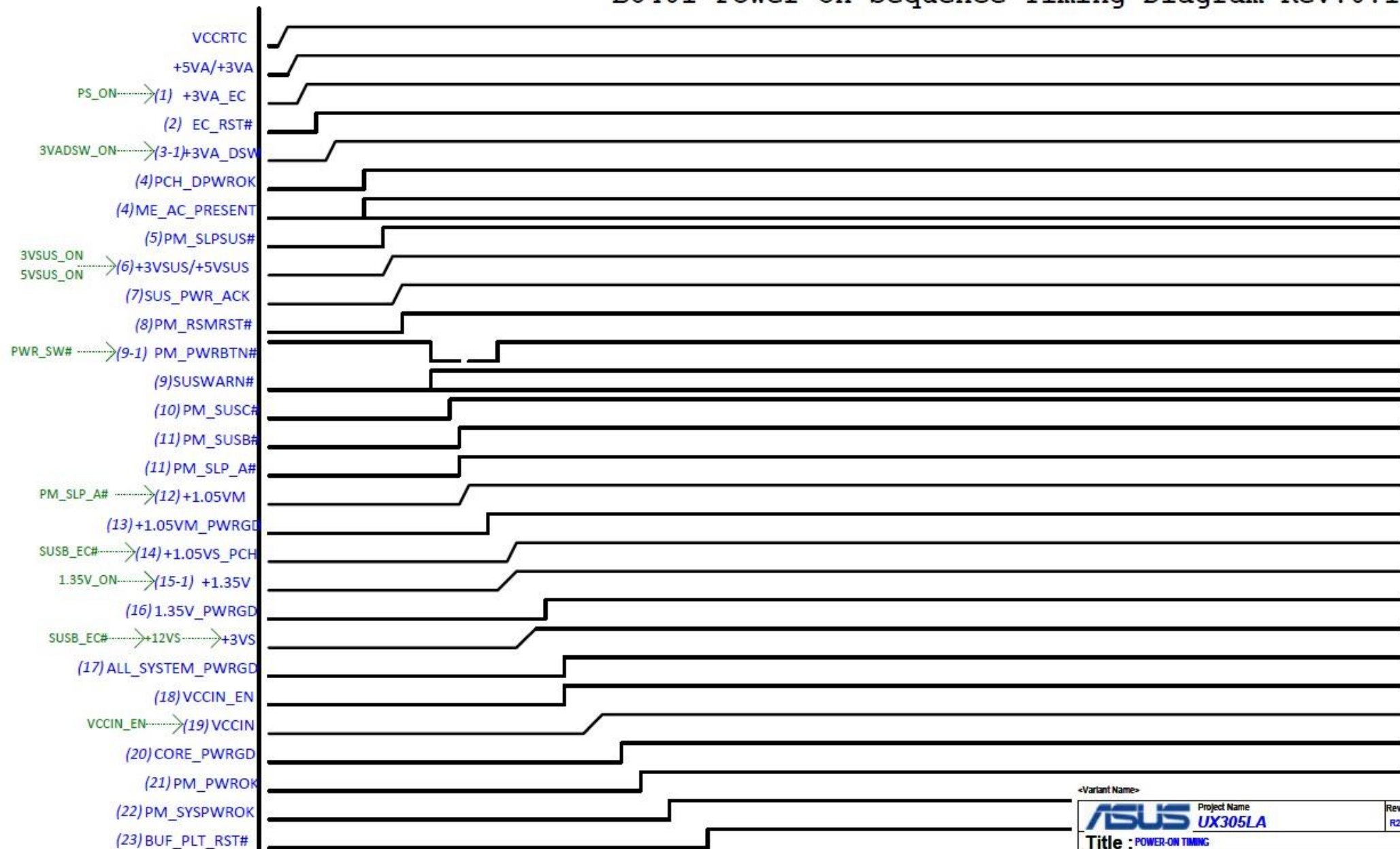
POWER ON SEQUENCE

PU551 Power On Sequence Diagram Rev.1.0



AC mode POWER ON SEQUENCE

BU401 Power-On Sequence Timing Diagram Rev.0.1



<Variant Name>

ASUS Project Name
UX305LA

Rev
R2.0

Title : POWER-ON TIMING

Signal Measure Point-Bottom

